

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Heinz Baier et al.
Serial No. : 09/637,214 Examiner : Unassigned
Filed : August 11, 2000 Art Unit : 2182
For : METHOD AND SYSTEM FOR PROGRAMMING
FPGAs ON PC-CARDS WITHOUT ADDITIONAL
HARDWARE



RECEIVED

April 1, 2002

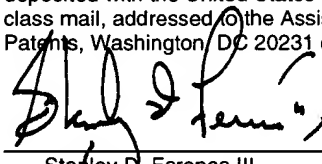
APR 12 2002

TRANSMITTAL LETTER

Technology Center 2100

I hereby certify that this correspondence and any documents referred to as enclosed therewith are being deposited with the United States Postal Service as first class mail, addressed to the Assistant Commissioner for Patents, Washington, DC 20231 on April 1, 2002.

Assistant Commissioner for Patents
Washington, DC 20231

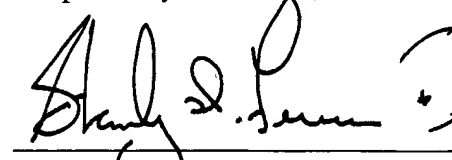

Stanley D. Ference III
Reg. No. 33,879

April 1, 2002
Date of Signature

Sir:

Transmitted herewith is an Associate Power of Attorney for the above-identified application.

Respectfully submitted,


Stanley D. Ference III
Registration No. 33,879

FERENCE & ASSOCIATES
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Enclosure

Attorneys for Applicants

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ASSOCIATE POWER OF ATTORNEY

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Assistant Commissioner for Patents
Washington, DC 20231

Technology Center 2100

Sir:

Please recognize the following individual as an Associate Attorney in this case:

Stanley D. Ference III
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All correspondence should continue to be directed to the above appointed
Associate Attorney.

Respectfully submitted,

Paul J. Otterstedt

Dated: MARCH 19, 2002

PAUL J. OTTERSTEDT
Registration No. 37,411

One of the Attorneys of Record